

Serial No. 10/647,468  
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**AMENDMENT TO THE SPECIFICATION**

The paragraph commencing at page 3, line 24 is amended as follows:

Fig. 10 illustrates the conventional differential band gap circuit. As shown in Fig. 10, the differential band gap circuit has a differential amplifier to be configured of a pair of p-type transistors P1 and P2, and a pair of n-type transistors N1 and N2. This differential amplifier has the gate of p-type transistor P3 connected between the p-type transistor P2 and the n-type transistor N2 thereof, and this p-type transistor P3 is connected to the output terminal VOUT.